**Face-To-Face Student/Supervisor Meeting Record**

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| **Project Title:** | Design and Implementation of a Multi-core Processor using FPGA | **Photo:** |  |
| **Student Name:** | Matteo Bovino | **Student ID:** | 8671055 |
| **Supervisor:** | Server Kasap | **Student UID:** |  |
| **Supervisor UID:** |  | **Department:** | AAEEE |
| **Course Code:** | Electrical and Electronic Engineering | **Module Code:** | 305AAE / 306AAE |
| **Date Today:** | 06/11/2020 | **Time:** | 13:00 AM |

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| **Current Progress and Issues:** | |
| *In this meeting we discussed about the structure for the literature review and the different ways to collect materials for a better academic work. We specified the general guideline to follow throughout the whole review, a framework that is based on providing a general understanding of the topics at hand without becoming excessively detailed. This point will also determine what type of references are going to be used. The aim is to enrich the document using strong references while not transforming it in a list of citations.* | |
| **Agreed Key Action Points:** | |
| *The main upcoming deadline is the one for the literature review (16/11/2020). With 10 days left, it is time to finalize the overall document. As agreed during the meeting, before submitting the final review, a draft will be shared with the supervisor in order to fix any issues or errors missed by the student. A new timetable for the meetings has also been agreed upon, this new schedule will allow for better communication between student and supervisor for the time being. Meanwhile, the other material will be covered in the specified manner in order to remain on track on every front.* | |
| **Date and Time of next meeting:** | 13:00 AM 20/11/2020 |

*Signatures of those present:*

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| **Supervisor:** |  |
| **Student:**  **Matteo Bovino** |  |